

FIG. 1 (Background art)

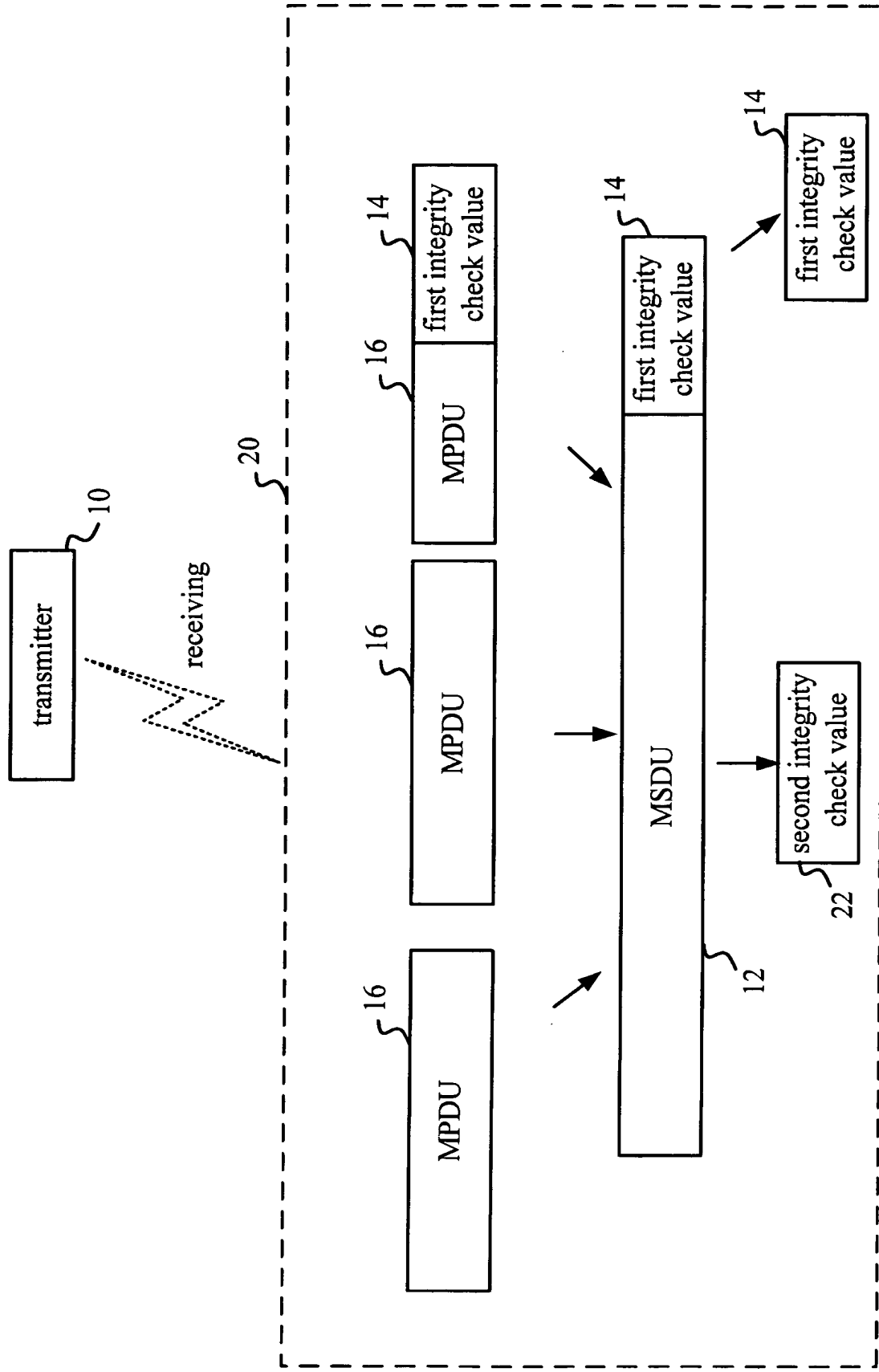


FIG. 2 (Background art)

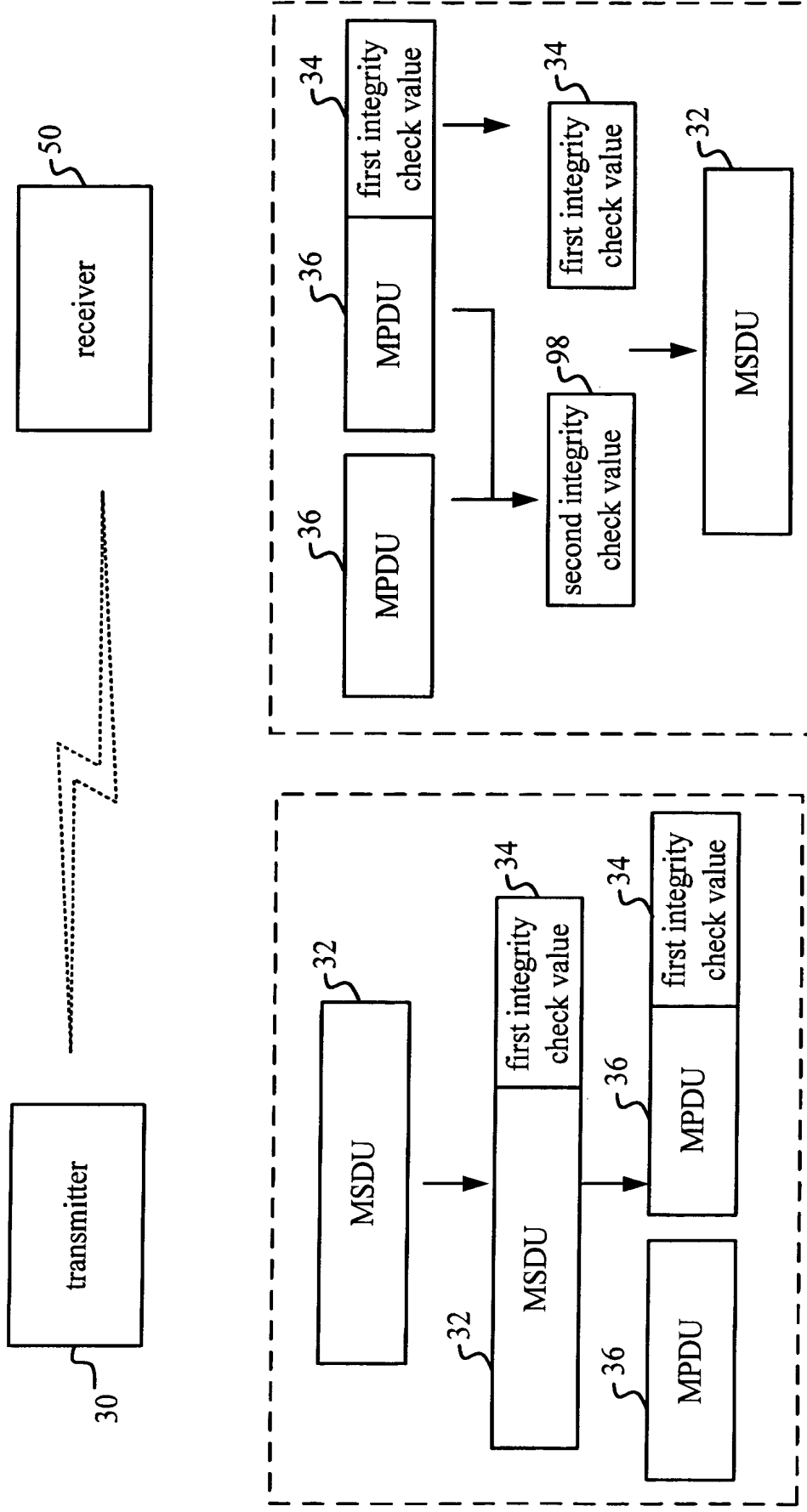


FIG. 3

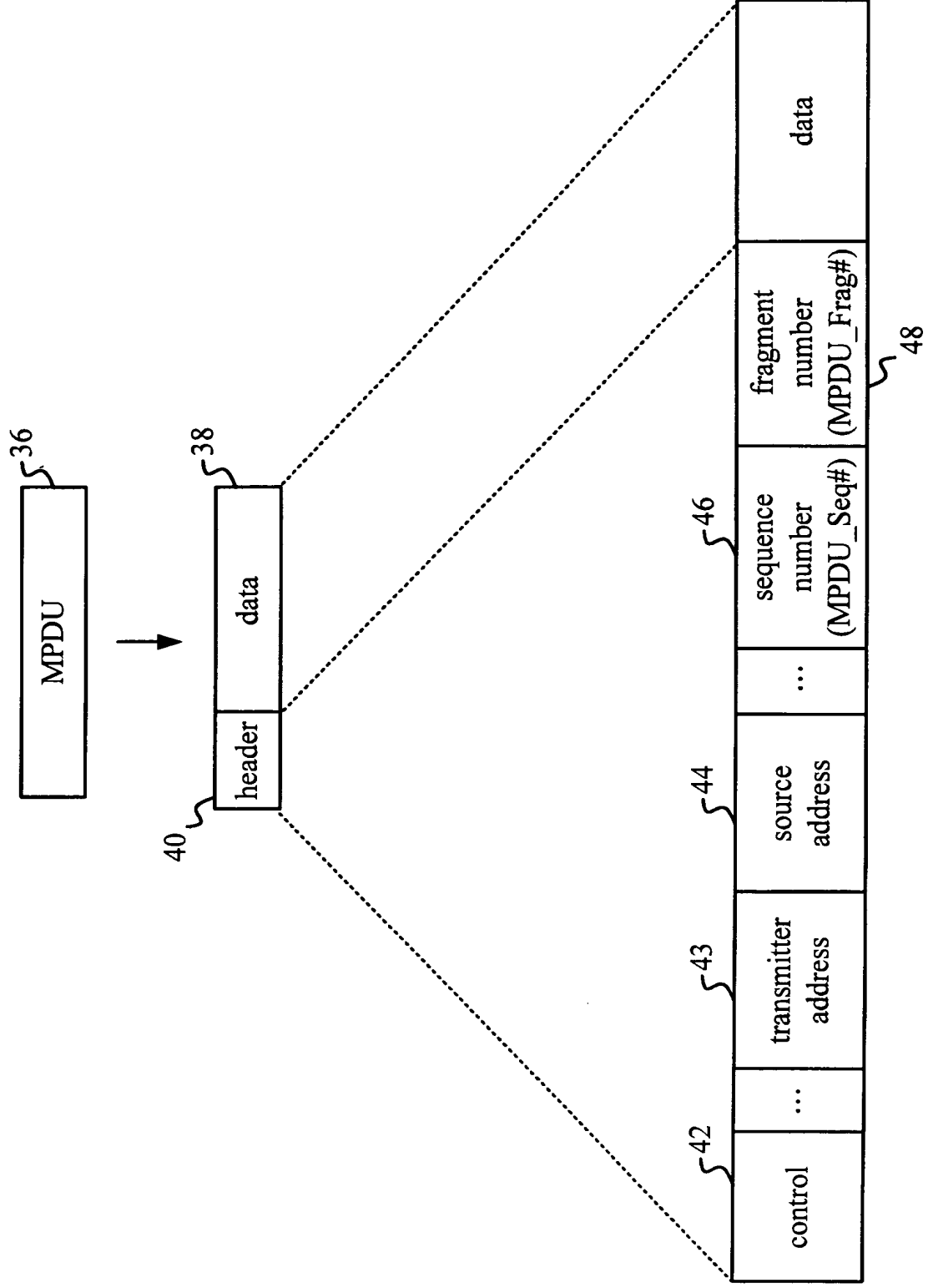


FIG. 4

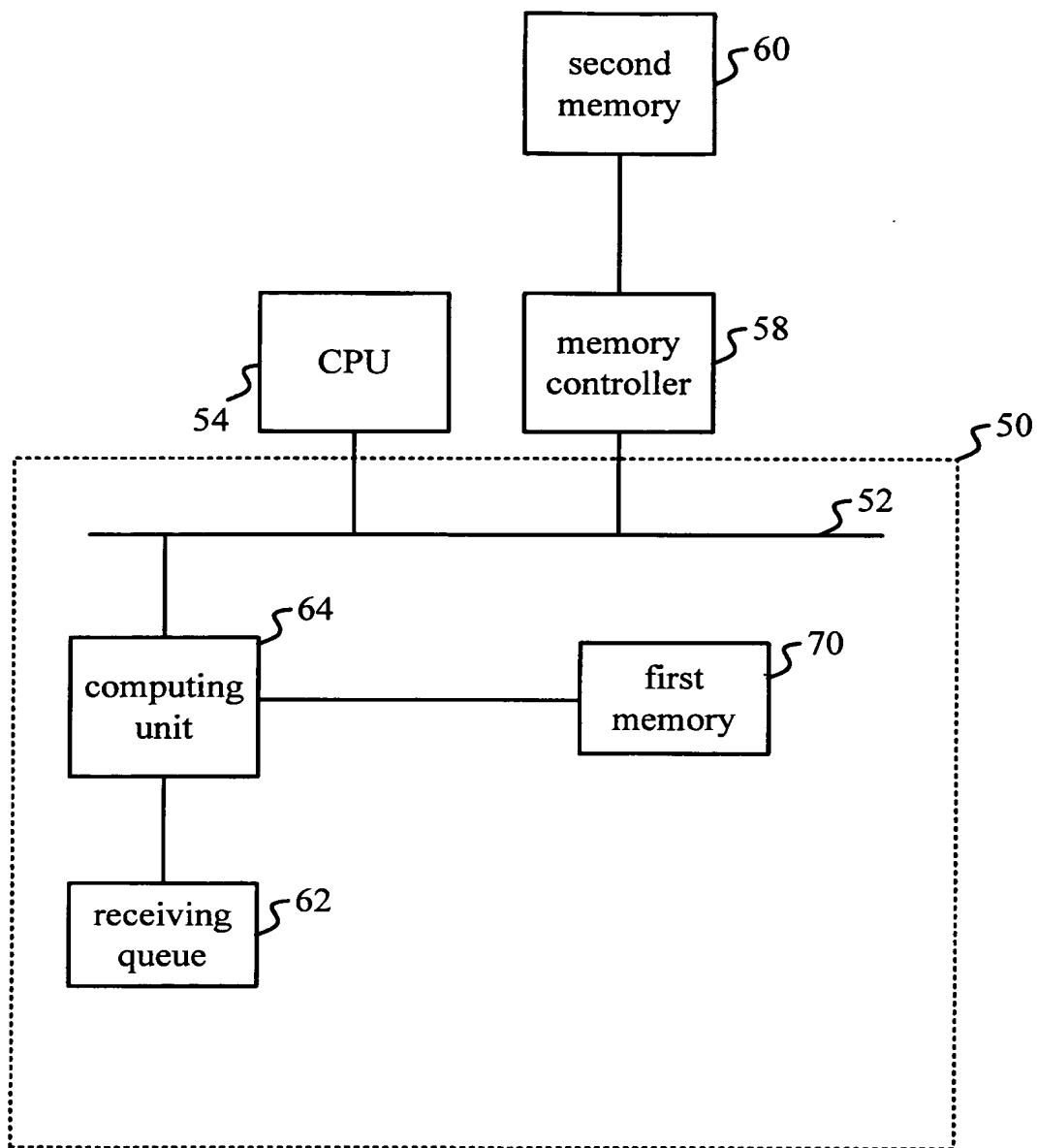


FIG. 5

	571		572		574		576		578	
	transmitter address	.....	sequence number (WKT_Seq#)	fragment number (WKT_Frag#)	key	temporary check value (TEMP_MIC)				
transmitter 1	AAAA	.....	0~4095	0~15	K#	####				
transmitter 2	...	...	...	...	...	...				
⋮	...	...	...	...	...	...				
transmitter n	...	...	...	...	...	...				

FIG. 6

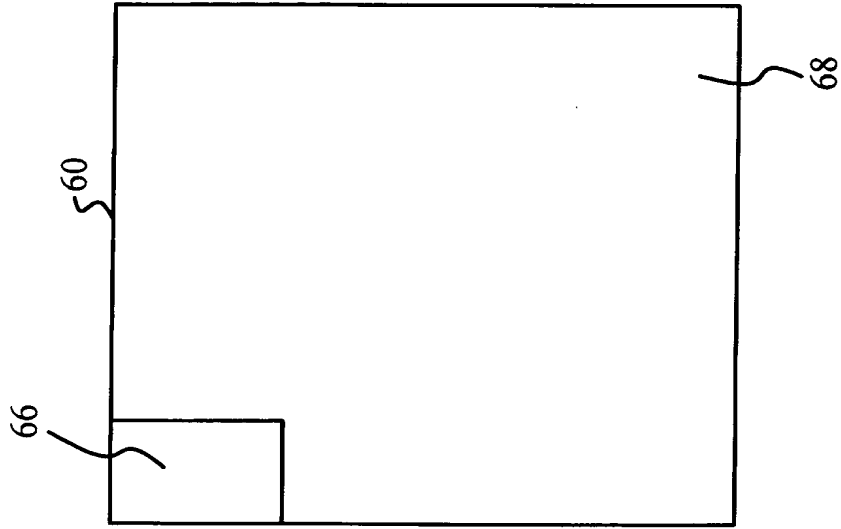


FIG. 7a

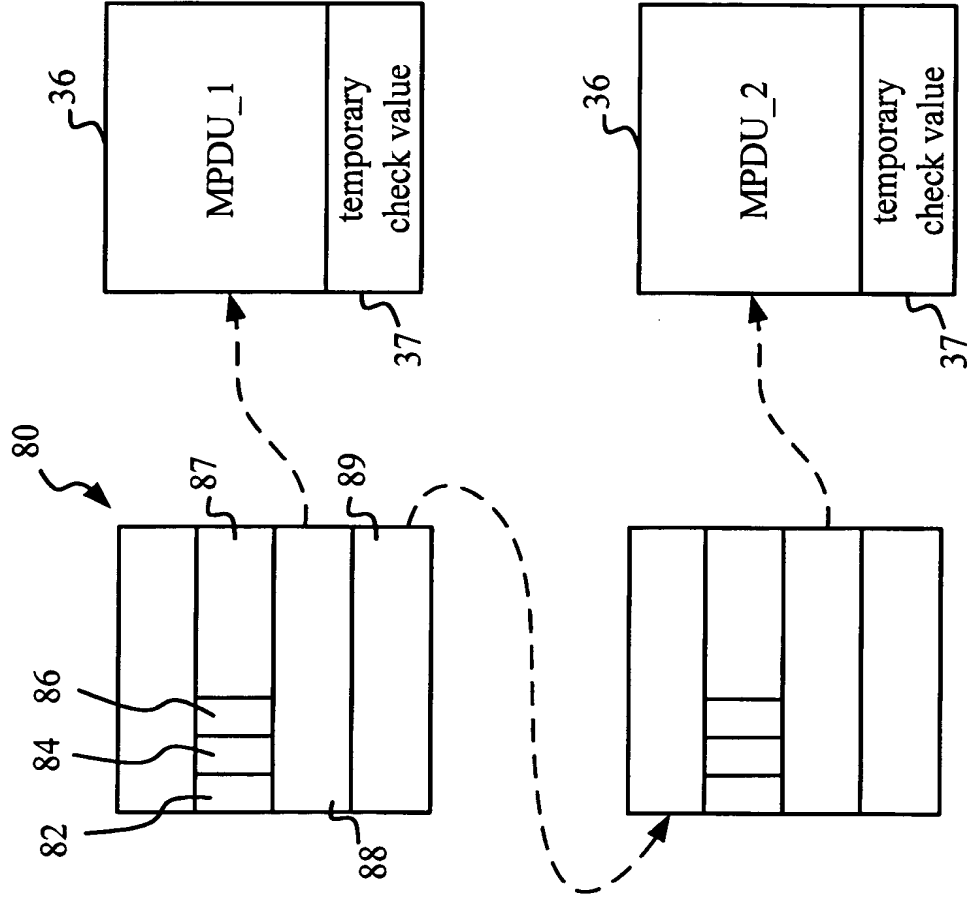


FIG. 7b

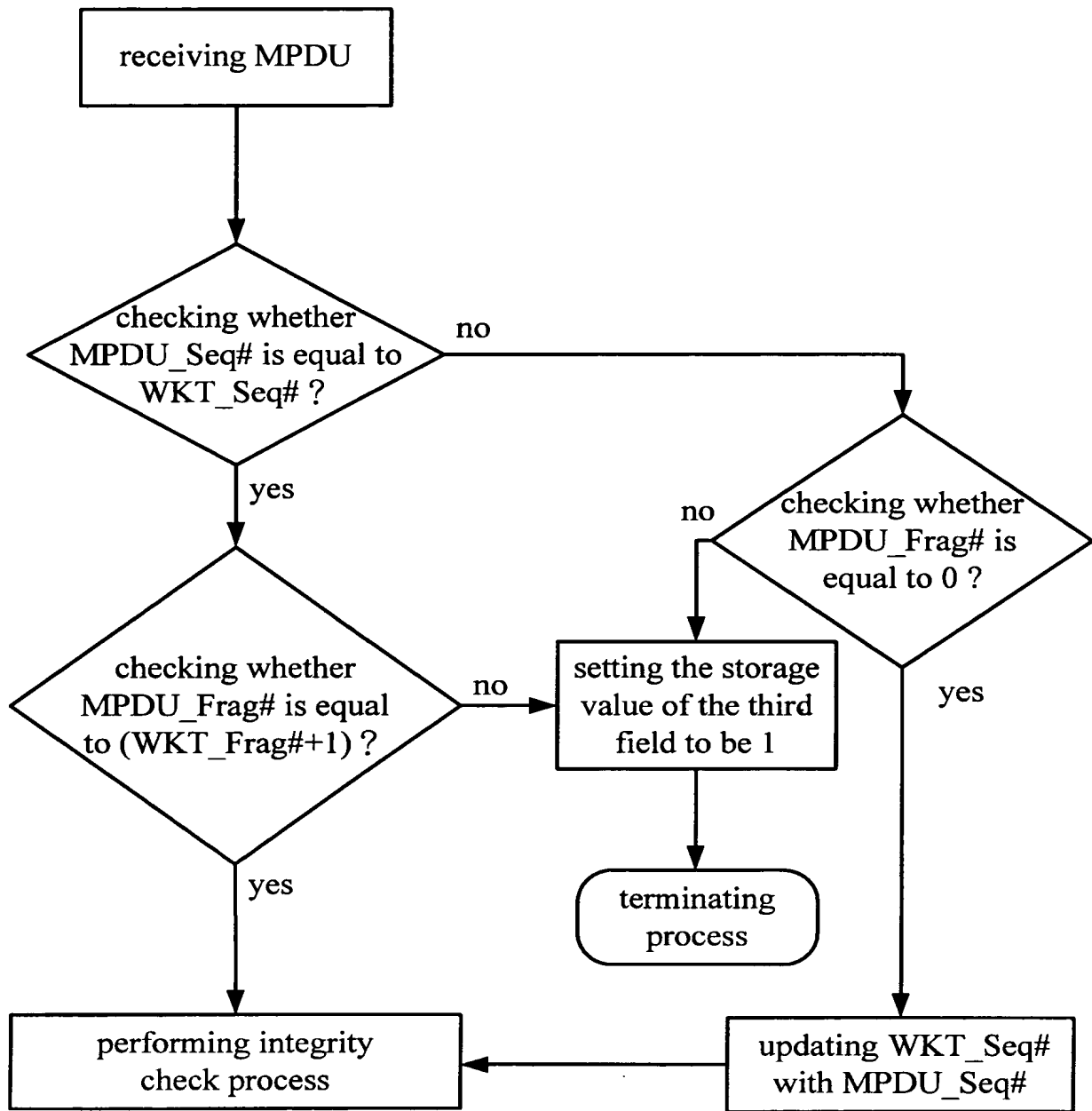


FIG. 8



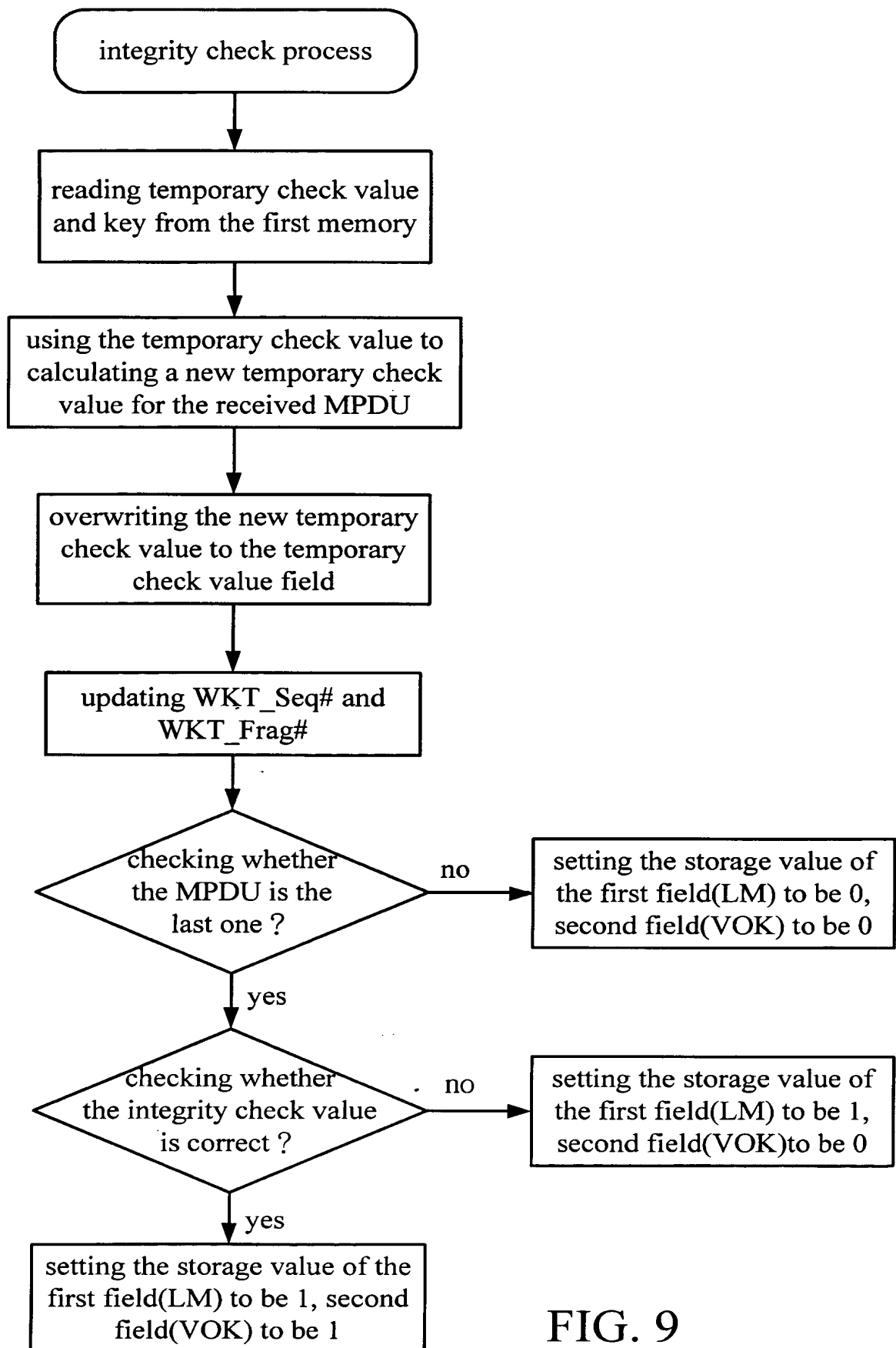


FIG. 9